EC Master

Quick Start Guide



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EtherCAT System Architecture





EC → Master

EC-Master Architecture







EC-Master Building Blocks







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EC-Engineer: Overview

- One single tool for EtherCAT configuration and diagnosis
- Perfect supplement to EC-Master
- Import of EtherCAT Slave Information (ESI) files
- Export of EtherCAT Network Information (ENI) file
- Register here to get a free evaluation version: <u>https://www.acontis.com/en/ecdownloads.html</u>

─ EC-Engineer [D:\Temp\config.ecc]			
<u>File View Settings H</u> elp			
Configuration Mode 🚽 Export ENI 🖉 Diagnos	sis Mode		
EtherCAT Network-Editor			
💌 🎚 Bus (Class-A Master)	General PDO Mapping V	ariables Advanced Options Distributed Clocks Init Commands CoE Object-Dictionary	
 Slave_001 [EK1100] (0001) Slave_002 [EL2008] (0002) Slave_003 [EL2008] (0003) 	Address Station Address	10	
Slave_003 [EL2003] (0003)		10	
Slave_005 [EL1008] (0005) Slave_006 [EL4132] (0006)	Information Name	Axis 1	
Slave_007 [EL3162] (0007)	Description	SGDV-E5 EtherCAT(CoE) SERVOPACK Rev3	
"# Slave_008 [EK1122-0080] (0008)	Vendor	Yaskawa Electric Corporation (0x539 / 1337)	
001 : Terminals [032-18830]	Product Code	0x2200002 (35651586)	
1 002 : Terminals [022-1BB20]	Revision Number	0x30005 (196613)	
G 003 : Terminals [040-1CA00]	ESI File	C:\ProgramData\ECEngineer\EtherCAT\Yaskawa SGDV-E5_CoE rev3.05.xml	
1. Axis 1 (0010)	Topology		
fl., Axis 2 (0011)	Connected Devices	[PORT B] - Axis 2 (0011)	
I., Axis 3 (0012)	Predecessor Device	Slave_009 [VIPA 053-1EC00] (0009) [PORT B]	
II,, Axis 4 (0013)			
CL. 1 1C. Bat View			
Classic View Trac View	Massage		
	Michagles		* *
Name Auto 1	Severity Time	Message	
Description SGDV_E5 EtherCAT(CoE) SERV/ODACK P	INF 08:17:06 Configuration saved to D:\Temp\config.ecc		
Vendor Varkawa Electric Corporation (0x530 / 13	Vendor Varkews Electric Composition (0x50) / 13		
Master/Bus: 1 Slaves: 17			Mode: CONFIG EXPERT











Generate bus configuration with EC-Engineer Step 1: Connect EtherCAT Slaves



- EC-Engineer comes with an integrated EtherCAT master for scanning the connected EtherCAT slaves
- Every Ethernet Network Interface with an valid Windows driver can be used
- A second, dedicated Network Interface for EtherCAT is recommend
- Warning: Do not connect any EtherCAT slaves to your Office LAN





Generate bus configuration with EC-Engineer Step 2: Install and start EC-Engineer



🛹 EC-Engineer []			
File View Network Settings Help	toda		
Project Evaluation Prode	Device Editor		
	Start Page		
	EC – Eng	Ineer	
	, U		
	Add Master Unit	Getting Started	
	EtherCAT Master Unit (Class A) EtherCAT Master Unit (Class B)	Ти-	Offline Configuration
	Arr EtherCAT Master Unit (Beckhoff CX5000)		In the Office
		EtherCAT	Online Configuration Slaves connected to Engineering System
			Remote Configuration
		EtherCAT	Slaves connected to Target System
		TCP/IP Master EtherCAT	Remote Diagnosis Slaves connected to Target System
Classic View Flat View Topology View			
Short Info	Messages		, 4
Information	Severity Time Message		
	INF 16:45:01 EC-Engineer ready. Version 2.	.4.0	
Networks: 0 Slaves: 0			State: 🧼 🧼 Mode: CONFIG STANDARD



Generate bus configuration with EC-Engineer Step 3: Select "Online Configuration" and "EtherCAT Master Unit (Class A)"



EC-Engineer []		
Configuration Mode	ode	
Project Explorer	Start Page EC Engineer Add Master Unit Getting Started	51
	Select Master Unit Dialog	Offline Configuration In the Office
	OK Cancel	Online Configuration Slaves connected to Engineering System
		Remote Configuration Slaves connected to Target System
Chesis View Flat View Topology View		Remote Diagnosis Slaves connected to Target System
Short Info	Messages	→ ₽
Information	Severity Time Message INF 16:45:01 EC-Engineer ready. Version 2.4.0	
Networks: 0 Slaves: 0		State: 🥥 🥥 Mode: CONFIG STANDARD



Generate bus configuration with EC-Engineer Step 4: Choose network adapter from list and press "Select"



📂 EC-Engineer []			_D×
<u>File View Network Settings Help</u>			
Configuration Mode 🛛 🚽 Export ENI 🖉 Diagnosis M	lode		
Project Explorer	Device Editor		
Ilass-A Master	Master		
	Caparal		
	Unit Name	Class & Master	
	Cycle Time [us]	1000	
	Source MAC address	1000	
	Slaves connected to local syste	em	
	Network Adapter	EtherCAT2 (Realtek RTL8168C(P)/8111C(P) Family PCI-E Gigabit Ethernet NIC (NDIS 6.20))	
			Select
	Slaves connected to remote sy	stem	
	IP Address	127 · 0 · 0 · 1	
	Port	6000	
	Master-Instance	0	Select
		Master-Instance of the remote system	
Classic View Flat View Topology View			
Short Info 🗶 🕂	Messages		→ ₽
Information	Severity Time Message		
Name Class-A Master	INF 16:45:01 EC-Engineer n	eady. Version 2.4.0	
Description EthorCAT Mactar Unit (Class A)			
Networks: 1 Slaves: 0		State: 🥥 🥥 Mode: CO	NFIG STANDARD



Generate bus configuration with EC-Engineer Step 5: The found slave devices are listed in the tree



Թ EC-Engineer []		
File View Network Settings Help		
Configuration Mode Report ENI Diagnosis	Mode	
Project Explorer	Device Editor	
U Class-A Master U Class-A Master U Class-A Master	Master Process Data Image	
Slave 1002 [FI 2004] (1002)	General	
Slave 1003 [EL2004] (1003)	Unit Name	Class-A Master
Slave_1004 [EL1014] (1004)	Cycle Time [us]	1000
Slave_1005 [EL1014] (1005)	Source MAC address	00-0A-CD-16-BD-DD
Slave_1006 [EL4132] (1006)		
Slave_1007 [EK1110] (1007)	Slaves connected to local sys	tem
	Network Adapter	EtherCAT2 (Realtek RTL8168C(P)/8111C(P) Family PCI-E Gigabit Ethernet NIC (NDIS 6.20))
		Deselect
	Slaves connected to remote s	vstem
	IP Address	127 . 0 . 0 . 1
	Port	6000
	Master-Instance	0 Select
Classic View Flat View Topology View		
Short Info	Messages	
Information	Severity Time Message	
Name Class-A Master	INF 16:49:01 Master state	change from 'Unknown' to 'Init'
Description EtherCAT Master Unit (Class A)		
Networks: 1 Slaves: 7		State: 💚 🧼 Mode: CONFIG STANDARD



Generate bus configuration with EC-Engineer Step 6: Export ENI file



🚧 EC-Engineer []		
<u>File View Network Settings Help</u>		
Configuration Mode	Mode	
Project Explorer Exports ENI file of selected de	wice Vice Editor	
Class-A Master	Master Process Data Image	
Slave_1001 [EK1100] (1001)	General	
Slave_1002 [EL2004] (1002)	Unit Name	Class-A Master
Slave 1005 [EL2004] (1005)	Cycle Time [us]	
Slave_1007 [EL1014] (1005)	Source MAC address	
Slave 1006 [EL4132] (1006)		00-04-02-10-02-02
Slave_1007 [EK1110] (1007)	Slaves connected to local sys	tem
	Network Adapter	EtherCAT2 (Realtek RTL8168C(P)/8111C(P) Family PCI-E Gigabit Ethernet NIC (NDIS 6.20))
		Deselect
	Slaves connected to remote a	system
	IP Address	127 . 0 . 0 . 1
	Port	6000
	Master-Instance	0 Select
Classic View Flat View Topology View		
Short Info	Messages	
Information	Severity Time Message	
Name Class & Master		a desare from 'the beaut' as 'to at'
Description EtherCAT Master Unit (Class A)	V INF 16:49:01 Master state	
Networks: 1 Slaves: 7		State: 💚 💚 Mode: CONFIG STANDARD



Generate bus configuration with EC-Engineer Step 7: Switch to "Diagnosis Mode" and set state to OPERATIONAL





EC
Master

Generate bus configuration with EC-Engineer Step 8: Bus is OPERATIONAL



EC-Engineer []				_	
Configuration Mode	lode				
Project Explorer Class-A Master <connected> Slave_1001 [EK1100] (1001) Slave_1002 [EI 2004] (1002)</connected>	Device Editor General Process Data Image Watc State Machine	h list	_		
 Slave_1003 [EL2004] (1003) Slave_1004 [EL1014] (1004) Slave_1005 [EL1014] (1005) 	Current State Requested State	Op Op Init Bootstrap			
 Slave_1006 [EL4132] (1006) Slave_1007 [EK1110] (1007) 	Change State Information	Pre-Op Safe-Op Op	Frame Counter		
	Number of found slaves Number of slaves in configuration Number of DC slaves	7 7 0	Sent frames Lost frames Cyclic frames	52532 0 52416	
	DC in-sync Topology Ok Link Connected	- Yes Yes	Acyclic frames	116	
	Slaves in Master State	Yes]		
Classic View Flat View Topology View					
Short Info	Messages Severity Time Message INF 16:52:50 Master state	change from 'Safe-Op' to 'Op'			↓ ↓
Networks: 1 Slaves: 7				State: 🔶 🌒 Mode: DIAGNOSIS STA	NDARD



Generate bus configuration with EC-Engineer Step 9: Switch back to "Configuration Mode" and Exit



🛹 EC-Engineer []		
File View Network Settings Help		
New Ctrl+N Export ENI Diagnosis	Mode	
Den Ctrl+O	Device Editor	
Lave Ctrl+S	Master Process Data Image	
Save As 00] (1001)		
Print L2004] (1002)	General	
Add Master-Unit L2004] (1003)	Unit Name	Class-A Master
L1014] (1004)	Cycle Time [us]	1000
L1014] (1005)	Source MAC address	00-0A-CD-16-BD-DD
L4132] (1006)		
→ Recent Projects	Slaves connected to local syst	em
Exit Alt+F4	Network Adapter	EtherCAT2 (Realtek RTL8168C(P)/8111C(P) Family PCI-E Gigabit Ethernet NIC (NDIS 6.20))
		Deselect
	Slaves connected to remote s	ystem
	IP Address	127 . 0 . 0 . 1
	Port	6000
	Master-Instance	0 Select
		·
Classic View Flat View Topology View		
Short Info 🚽 🕂	Messages	→ #
Information	Severity Time Message	
Name Class-A Master	INF 16:53:27 Master state	change from 'Op' to 'Init'
Description EtherCAT Master Unit (Class A)		
Networks: 1 Slaves: 7		State: 🗼 🧼 Mode: CONFIG STANDARD





- Extract/Install the EC-Master by running setup.exe
 - Note: Some platforms as ZIP/tar.gz archive, not as setup.exe
- The documentation including this Quick Start Guide is in folder "Doc"
- There is an EC-SlaveTestApplication starter in the Windows start menu



Operate slaves with EC-STA Slave Test Application Step 1: Start EC-STA from the start menu and configure



<u>File Log H</u> elp
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EC-SlaveTestApplication V2.8.1.0	
<u>File Log H</u> elp	
B Master	Configuration Master Slave Process Data CoE FoE AoE SoE EEPROM Notifications DemoMotion
Slave_1001 [EK1100]	Initialize Master with configuration parameter
Slave_1003 [EL2004]	Initialize De-Initialize
Slave_1005 [EL1014]	
Slave 1007 [EK1110]	
	Init Pre-Op Safe-Op Operational 2
	Current State : OP State Change Timeout
	Requested State : OP 15.000
	Bus ropology
	Scan Bus Scan Bus Imeout : 3.000
	Connected Slaves : 7 Configured Slaves : 7
	Result : SUCCESS
1	
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ochnologies	
C-Master V2.8.1.01 (Eval) fo	r Windows_x86 Copyright acontis technologies GmbH @ 2016
cLinkOpen(): Use network ada	pter "Realtek RTL8168C/8111C PCI-E Gigabit Ethernet NIC"
nlicensed version, stop send	ing ethernet frames after 60 minutes! s found
aster state changed from <un< td=""><td>KNOWN> to <init></init></td></un<>	KNOWN> to <init></init>
Master state changed from <un Master state changed from <in Master state changed from <pr< td=""><td>KNOWN> to <init> II> to <preop> EOP> to <safeop></safeop></preop></init></td></pr<></in </un 	KNOWN> to <init> II> to <preop> EOP> to <safeop></safeop></preop></init>
Master state changed from <un Master state changed from <in Master state changed from <pr Master state changed from <sa< td=""><td>KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init></td></sa<></pr </in </un 	KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init>
Master state changed from <un Master state changed from <in Master state changed from <pr Master state changed from <sa< td=""><td>KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init></td></sa<></pr </in </un 	KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init>
Master state changed from <un Master state changed from <in Master state changed from <pr Master state changed from <sa< td=""><td>KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init></td></sa<></pr </in </un 	KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init>
Master state changed from <un Master state changed from <in Master state changed from <pr Master state changed from <sa< td=""><td>KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init></td></sa<></pr </in </un 	KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init>
Waster state changed from <un Waster state changed from <in Waster state changed from <pr Waster state changed from <sa< td=""><td>KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init></td></sa<></pr </in </un 	KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init>
aster state changed from <un aster state changed from <in aster state changed from <pr aster state changed from <sa< td=""><td>KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op> to <op></op></op></safeop></preop></init></td></sa<></pr </in </un 	KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op> to <op></op></op></safeop></preop></init>



Operate slaves with EC-STA Slave Test Application Step 3: Do further tests, e.g., flashing outputs



	EC-SlaveTestApplication V2.8.1.0			
	<u>File Log H</u> elp			
	⊟	Configuration Master Slave Process Data CoE FoE AoE SoE EEPROM Notifications DemoMotion		
1	Slave_1001 [EK1100] Slave_1002 [EL2004] Slave_1003 [EL2004] Slave_1003 [EL2004] Slave_1005 [EL1014] Slave_1005 [EL1014] Slave_1005 [EL4132] Slave_1007 [EK1110]	Slave (Fixed Address): 1002 Slave (Fixed Address): 1002 Slave State Init Pre-Op Safe-Op Operational Boot Strap State Change Timeout 3.000 Image: State Change Timeout Image: State Change Timeout Current State : OP Requested State : OP Process Data Process Data		
		Status : jivo error Output Offset (Byte / Bits) Size (Byte / Bits) (Flash		
		Slave Information 0.0 / 0.0 / 0.4 / 4		2
	acontis	Vendor ID : [0x2 Productcode : [0x7D43052 Revision : [0x110000 Serial : [0x0 Address : [0x3EA Alias : [0x0 Config Address : [0x3EA Hot Connect Present THC Group		
	EC-Master V2.8.1.01 (Eval) fo EcLinkOpen(): Use WinPcap ver EcLinkOpen(): Use network ada Unlicensed version, stop send Bus scan successful - 7 slave Master state changed from <un Master state changed from <im Master state changed from <pr Master state changed from <sa< th=""><th>r Windows_x86 Copyright acontis technologies GmbH @ 2016 sion 4.1.3 (packet.dll version 4.1.0.2980), based on libpcap version 1.0 branch 1_0_rel0b (20091008) pter "Realtek RTL8168C/8111C PCI-E Gigabit Ethernet NIC" ing ethernet frames after 60 minutes! s found KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init></th><th>×</th><th></th></sa<></pr </im </un 	r Windows_x86 Copyright acontis technologies GmbH @ 2016 sion 4.1.3 (packet.dll version 4.1.0.2980), based on libpcap version 1.0 branch 1_0_rel0b (20091008) pter "Realtek RTL8168C/8111C PCI-E Gigabit Ethernet NIC" ing ethernet frames after 60 minutes! s found KNOWN> to <init> IT> to <preop> EOP> to <safeop> FEOP> to <op></op></safeop></preop></init>	×	
	Masterstate: 🌒 Operational Not Opera	tional Slaves : 0000 Frames Lost : 0000 Cyclic WKC Errors : 0000	.::	



Connect EC-Engineer with EC-STA Application Step 1: Start EC-Engineer and select "Remote Diagnosis"



EC-Engineer []			
He View Network Settings Help	1ode		
Project Explorer	Device Editor		3
	Start Page		
		ninoor	
	EU	lineer	
	Add Master Unit	Getting Started	
	EtherCAT Master Unit (Class B) EtherCAT Master Unit (Beckhoff CV5000)		Offline Configuration
			In the onice
			Online Confirme Vice
			Slaves connected to Engineering System
		TCP/IP Master EtherCAT	Remote Configuration Slaves connected to Target System
			Pemote Diagnosis
		TCP/IP Mester EtherCAT	Slaves connected to Target System
Classic View Flat View Topology View			
Short Info	Messages Severity Time Message		- -
	INF 17:00:16 EC-Engineer ready. Version	2.4.0	
Networks: 0 Slaves: 0			State: 💚 🌒 Mode: CONFIG STANDARD



Connect EC-Engineer with EC-STA Application Step 2: Choose "Slaves connected to remote system"



🛹 EC-Engineer []			_O×					
File View Network Settings Help	lode							
Project Explorer Class-A Master	Device Editor Master General							
	Unit Name	Class-A Master						
	Cycle Time [us] Source MAC address	1000	<u> </u>					
	Slaves connected to local syst Network Adapter	tem EtherCAT2 (Realtek RTL8168C(P)/8111C(P) Family PCI-E Gigabit Ethernet NIC (NDIS 6.20))	Select					
	Slaves connected to remote system							
	IP Address							
	Master-Instance	0	Select					
Classic View Flat View Topology View								
Short Info 🗸 🕂	Messages		• 					
Information Name Class A Master	Severity Time Message	razdy Varcian 2.4.0						
Description EtherCAT Master Lipit (Class A)	The 17:00:10 EC-Engineer							
Networks: 1 Slaves: 0		State: 🧼 🧼 Mode: CC	ONFIG STANDARD					



Connect EC-Engineer with EC-STA Application Step 3: Check input or output variables



🛹 EC-Engineer []						
File View Network Settings Help	Iode					
		_	_	_		<u>.</u>
Class-A Master <connected></connected>	General Variables ESC Register EEPROM Extended Diagnosis DC Diagnosis					
 Slave_1001 [EK1100] (1001) 						
Slave_1002 [EL2004] (1002)	Variables					
Slave_1003 [EL2004] (1003)	Name	Datatype	Group Info	Offset	 Size 	Value Forced
Slave_1004 [EL1014] (1004)	Slave_1002 [EL2004].Channel 1.Output	BOOL	[Default]	OUT :	0.0 0.1	0
Slave_1005 [EL1014] (1005) Slave_1006 [EL4132] (1006)	Slave_1002 [EL2004].Channel 2.Output	BOOL	[Default]	OUT :	0.1 0.1	1
Slave_1007 [EK1110] (1007)	Slave_1002 [EL2004].Channel 3.Output	BOOL	[Default]	OUT :	0.2 0.1	0
	Slave_1002 [EL2004].Channel 4.Output	BOOL	[Default]	OUT :	0.3 0.1	0
	Chart					
	Edit Variable				Add to	watch list
	Value: False		~	Force	Re	ease
Classic View Flat View Topology View						
Short Info 🔶 🖵	Messages					↓ ₽
Information	Severity Time Message					
Name Slave_1002 [EL2004]	INF 17:00:16 EC-Engineer ready. Version 2.4.0					
Networks: 1 Slaves: 7				State: 🧶	Mode:	DIAGNOSIS EXPERT



Next Steps → EC-Master Class B User Manual



- Run EcMasterDemo on your target system
 - 2 Getting Started
 - 2.3 Operating system configuration
 - 2.3.1 Link Layer selection
 - 2.4 Running EcMasterDemo
- Learn more about EcMasterDemo and the application framework
 - 3.1 Application Framework
- Development and runtime licensing process
 - 1.4 Protected version

