

acontis technologies GmbH

SOFTWARE



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1 Introduction

1.1 The ACONTIS RTOSWin Product Family

The ACONTIS RTOSWin family is a family of Windows virtualization solutions for multiple real-time operating systems and Microsoft Windows.

The key component of all these solutions is the RTOS Virtual Machine. The real-time operating systems are executed on top of the RTOS VM.



acontis technologies GmbH is providing a special VxWorks BSP. Using this BSP together with the RTOS VM runtime VxWorks can be executed together with Windows.

More details about the virtual machine can be found in the RTOS VM User Manual.

- Operating Modes
- Realtime Device Management (how to control hardware)
- RTOS VM configuration
- Booting the RTOS
- Communication Services: The RTOS Library

1.2 The ACONTIS RTOS Virtual Machine

The ACONTIS RTOS-VM provides a lightweight real-time virtualization platform for Windows.

On top of this platform either real-time firmware, custom or off-the-shelf real-time operating systems can be executed.

When using multicore CPUs one can choose between two general operation modes. A more detailed description about possible operation modes can also be found in the RTOS VM User Manual.

1.2.1 Shared Mode Operation

Windows shall run on all CPU cores and only one CPU core shall additionally run the real-time software. If the Windows application needs a lot of CPU power (e.g. for image processing) this will be the appropriate operation mode even on multi-core CPUs. In shared mode operation, Windows (on this core) will usually only get CPU time when the real-time software is idle.

The following diagram illustrates the flow of control:



Operating states of the RTOS-VM in shared mode

- ① Exception-handling or a higher priority interrupt becomes outstanding.
- ② Interrupt Service Routine optionally starts a new task and then finishes.
- ③ From the idle-state, VxWorks transfers control to Windows operating system.

Note: When running the RTOS-VM in shared mode on multiprocessor/multicore systems this state diagram is only applicable for one CPU core in the system (by default on the first core). All other CPU cores will run Windows only.

1.2.2 Exclusive Mode Operation

Windows and the real-time software shall run fully independently on different CPU cores. Using this mode will lead to much shorter interrupt and task latencies, as there is no need to switch from Windows to the real-time software.

The following diagram, illustrates the flow of control on a dual core system:



Operating states of the RTOS-VM in exclusive mode

- ① Exception-handling or a higher priority interrupt becomes outstanding.
- ② Interrupt Service Routine optionally starts a new task and then finishes.

Note: When running the RTOS-VM in exclusive mode Windows will never be interrupted. Application and interrupt processing run concurrently and independently on both CPU cores. There is no need in the real-time software to enter the idle state.

1.3 VxWin Architecture Overview

VxWin is split into two main components:

- a) The ACONTIS RTOS Virtual Machine runtime (VMF runtime)
- b) The VxWorks BSP. This component is provided by acontis technologies.



1.4 Components

1.4.1 Development components

To develop software for VxWin the following components are provided.

- a) VxWin BSP for VxWorks
 - Important:

The VxWin BSP is not part of the VxWin setup program. Please contact Customer Support for the latest VxWin BSP adequate to your desired WindRiver® VxWorks version.

- b) VxWin development components (SDK + Documentation)
 - The following files are shipped with VxWin for development support: - VxWin User Manual
 - RTOS VM User Manual (basic technology description)
 - RTOS Library for Windows/RTOS communication
 - SDK with Example applications

1.4.2 Runtime components

The runtime components are split into the following main parts:

- a) VxWin runtime components (ACONTIS RTOS Virtual Machine)
- b) Configuration files. For more information about the Configuration files, see chapter 2.1.
- c) VxWorks VxWin runtime images (this is the image built by VxWorks + VxWin BSP)

2 VxWin configuration settings

2.1 VxWin.config – the VxWin configuration file

All VxWin configuration settings are stored in the file VxWin.config (e.g. C:\Program Files\acontis_technologies\VxWin\Bin\Windows\Config\). If you wish to use a different configuration file, you may specify another filename and/or a different path by using the *-config* option of the Uploader command.

General configuration settings are described in the RTOS VM User manual (e.g. memory configuration, operation mode).

2.1.1 Syntax

The configuration file is an ASCII file that can be modified with a simple editor. Its syntax is similar to that of a Windows registry file.

2.1.1.1 General rules

- The first entry in a configuration file must be *RtosConfig*.
- Because comments are introduced by a semicolon (;), all characters following a semicolon will be ignored.
- No single line in the configuration file may exceed 256 characters.

2.1.1.2 Keys, Entries, and Values

The configuration settings are stored using keys. Specific settings are stored in entries. Every entry has both a name and a value. Every entry is subordinated to a specific key.

Example:

[Key]

"EntryName"=EntryValue

There are different types of values:

• <u>Single hexadecimal value</u> (dword)

"EntryName"=dword:1F

<u>Strings</u>

"EntryName"="This is a string"

<u>Multiple Strings</u>

"EntryName"=multi_sz:"First string","Second string","Last string"

<u>Multiple hexadecimal values</u>

"EntryName"=hex:XX,YY,...,ZZ

2.1.1.3 Include statements

Configuration commands and parameters can be split into multiple files. You may use an *include* statement to incorporate other configuration files into *VxWin.config*.

Examples:

```
#include "myOs.config"
#include "myApp.config"
```

Important notes:

- If no additional path information is given, included files must be located in the same directory as *VxWin.config*.
- Since nested includes are not supported, *include* statements may only be used in the main configuration file.

Common configuration files:

Filename	Meaning
VxWin.config	Main configuration file – contains includes to other configuration file(s) and maybe some common parameters
VxWinSmp.config	Configuration file used for SMP – contains additional IPI configuration.
General.config	General configuration file – contains information for the Uploader Utility

2.2 VxWin specific parameters

The user may define the following entries for VxWin under the [*rtos*] key:

Entry Name	<u>Type</u>	Description
Bootline	String	VxWorks boot line.
Console	string	VxWorks device name which shall be used for the console interface
VnetMACAddress	string	Virtual Network Adapter MAC address.
VnetPollPeriod	dword	Virtual Network Adapter polling period in timer ticks. 0 enabled interrupt mode
VnetNumCluster	dword	Number of network clusters for the Virtual Network Adapter.
AddNetworkX	string	Additional network interface where the IP stack shall be attached to. X==0 (AddNetwork0): first additional interface X==1 (AddNetwork1): second additional interface
LogNetworkInterfaceX	string	Network interface for which the network packet logger shall be enabled. X==0 (LogNetworkInterface0): first interface for packet capturing.
LogNetworkFileX	string	File where the network packet log shall be stored.
LogNetworkFileMaxSizeX	dword	Maximum capture file size in kByte

2.2.1 Bootline

As shown in the following example, the user may override the default VxWorks boot line by coding his/her own under the [*rtos*] key:

[rtos]
"Bootline" = "vnet(0,2)pc:vxWorks h=192.168.0.1 e=192.168.0.2 u=target pw=vxworks"

2.2.2 Console

By default the last serial channel is connected with the RTOS VM's virtual I/O channel. The VxWorks console will be redirected to the Target Console Window through the config file entry [Rtos] "Console"="/vio/0".

Alternatively the console can be redirected to any serial port – for example first port – using "/tyCo/0".

2.2.3 VnetMACAddress

The Virtual Network adapter address [also known as the MAC (Media Control Address) address] is generally specified in the following format: AA-BB-CC-DD-EE-02

2.2.4 VnetPollPeriod

The Virtual Network driver by default operates in polling mode. The polling period can be adjusted using this parameter. It is set in units of the system clock timer. A values of 0 enables the interrupt mode.

2.2.5 VnetNumCluster

Cluster size of the Virtual Network driver. In rare cases where extensive network traffic occurs this parameter has to be increased.

2.2.6 AddNetworkX

By default the IP stack will be attached to the virtual network. If additional network adapters shall be under control of VxWorks these adapters can be automatically attached to the IP stack using this parameter (the X has to be replaced by contiguous number beginning with 0). The following example will attach the fei0 device (first instance of the Intel PRO/100 network adapter) to the IP stack. The IP address and subnet mask will be set accordingly.

[rtos] "AddNetwork0"="fei0:192.168.100.1:255.255.255.0"

2.2.7 LogNetworkXxx

VxWin supports network packet logging which may help in solving network problems. Parameter *LogNetworkInterfaceX* selects the network interface where packets shall be captured. All captured packets will be stored in a file selected by parameter *LogNetworkFileX*. The maximum size of the file is limited by parameter LogNetworkFileMaxSizeX (kByte). The format of the file is compatible with PCAP.

3 Tutorials

3.1 Running a shipped VxWorks image and start the RealtimeDemo application

This tutorial shows how to run a shipped VxWorks image and start the RealtimeDemo example application. It assumes that VxWin is installed on the PC.

It assumes that VXW1n 1s installed on the PC.

- Start the System Manager
 → if it's the first launch of the System Manager opens a dialog to enter a workspace directory.
- Select "My Computer" node on the treeview and add an RTOS to the configuration.

🟓 System Manager V2.4 Build 4 (64-Bi	t Edition)		
<u> </u>			
≈ ≤ ⊮ 🖌		J)	10
 My Computer Global Settings CPU Assignment Realtime Optimizations Devices Files Memory Mapped Files Memory Areas 	Add RTOS	ly Computer dd a RTOS first.	
Show All Devices	RTOS Stopped	Workspace C:\Prog	gram Files\acontis_tech

• Choose one of the shipped RTOS images to be used.

Select the RTOS #1 section and push the "Start this RTOS" button.					
🟓 System Manager V2.4 Build 4 (64-Bit	t Edition)				
<u>F</u> ile <u>V</u> iew <u>T</u> ools <u>R</u> un <u>H</u> elp					
≈ 🖗 🖌	5	and the second s			
 My Computer Global Settings CPU Assignment Realtime Optimizations Devices Files Memory Mapped Files Memory Areas RTOS #1 (VxWorks 6.9) Settings 	RT Op To - A - C Remove RTOS Remove RTOS Start this RTOS Stop this RTOS Stop this RTOS	FOS #1 (VxWorks 6 en this node to setup the RTOS fully setup this RTOS, you neer ssign one or more devices to t heck RTOS RAM and CPU Assi	.9) 5. d: he RTOS: <u>Go</u> gnment settings: <u>Go</u>		
Show All Devices	RTOS Stopped	Workspace C:\Program Fil	es\acontis_technologies\VxWin\wc		

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• Press "Show Console" to see the Target Console Window:

```
_ 🗆 📈
Puttytel
VxWorks 6.9
                              KERNEL: WIND version 2.13
Copyright Wind River Systems, Inc., 1984-2014
CPU: VxWin. Processor #2.
Memory Size: 0x1000000 (16Mb). BSP version 6.9/2.
Created: Jan 30 2015 14:49:44
ED&R Policy Mode: Deployed
WDB Comm Type: WDB_COMM_END
WDB: Agent Disabled.
-> demoStart
              : Frequency 2834000000 Hz; Maximum value: 18446744073709551615
HPET
             : Not available.
MPERF / APERF : Available - processor frequency changes can be detected.
SMI counter
            : Not available.
TS measurement : Enabled
Auxiliary Clock: Enabled
TmrHw EarlyCall: True
IRQ
             : 1000
ClockRate
Delay Limit
tVxWinTimerEx task not started (call demoEnableTimerEx before demoStart)
Demo started,
NOTE: if the irg rate is too high, the system will become instable
because of vxWorks queue overflow
value = 116 = 0x74 = 't'
-> Timer ISR number = 7001
Interrupt delay [usec] (Min/Avg/Max) = 0.8 / 1.0 /
                                                     6.3
Timer interrupt period [usec] (Cur/Min/Max):
via TSC = 1000.0 / 994.7 / 1005.3
via HPET = not available!
CPU clock rate [%] (Cur/Min/Max) = 70.59 / 70.58 / 70.60
SMI occurrence = not available!
IO loop [usec] (Min/Max) = 49.8 /
Task Delay [usec] (Min/Avg/Max) = 2.0 / 2.3 / 15.0
Re-Scheduling = 0 IRQ = 0 TaskCount = 381615
VxWin Date = 03.02.2015 13:22:04
```

Enter "demoStart" to start the Realtime Demo application. The measure can be reset by entering "demoReset" and stopped by "demoStop".

3.2 How to build the VxWorks example applications

The different examples are described in detail in chapter 4 Example Applications. In the examples folder is additional a file HowToBuildTheExamples.txt. This file includes a short description how to build the examples for every example application.

3.3 How to make your own VxWin Image (BSP)

The VxWin BSP for VxWorks depends on your VxWorks version and is delivered separately and not part of the VxWin Setup. See chapter "5.7 VxWin – Board Support Package" for additional information.

• Please copy the VxWin BSP files into your other VxWorks "...target\config" directory:

🔄 C:\¥X69GPP\vxworks-6.9\target\o	:onfig\¥xWin			_ 🗆 🗵
<u>File E</u> dit <u>V</u> iew F <u>a</u> vorites <u>T</u> ools	Help			
🚱 Back 🔹 🕥 🖌 🏂 🔎 Searc	h 🔀 Folders	B 🕑 🗙 🍤 💷-		
Address 🗁 C:\VX69GPP\vxworks-6.9\tar	get\config\V×Win			💌 🄁 Go
Folders	×	Name 🔺	Size	Туре
D-C vxworks-6.9	_	🚞 src		File Folder
H Catalight	_	🖬 00bsp.cdf	12 KB	CDF File
a dettingStarted		🖬 00html.cdf	1 KB	CDF File
E host		🖬 00V×Win.cdf	6 KB	CDF File
🗄 🛅 man		🖬 01V×Win.cdf	2 KB	CDF File
- 🛅 setup		🖬 02V×Win.cdf	6 KB	CDF File
⊞ 🛅 share		🖬 03V×Win.cdf	1 KB	CDF File
🖃 🛅 target		🖬 04V×Win.cdf	2 KB	CDF File
🗄 🛅 3rdparty		🖬 05V×Win.cdf	1 KB	CDF File
⊡ • 🛅 confia		🖬 20bsp.cdf	6 KB	CDF File
		🔤 20comp_tipc_hend.cdf	3 KB	CDF File
🗄 🗌 🔄 🗄 📩 🔂 🗄 🔂		🔤 22comp_mipc.cdf	2 KB	CDF File
⊡ ⊡ comps		99V×WinHibernate.cdf	2 KB	CDF File
	i 📕	🛅 config.h	43 KB	C/C++ Header
itl atom		onfigHtml.h	2 KB	C/C++ Header
itl core2		🛅 configInum.h	14 KB	C/C++ Header
itl core2 6	4	b configNet.h	5 KB	C/C++ Header
⊡ ⊡ itl crownbe	ach	🖸 hwconf.c	23 KB	C Source
⊡ ⊡ ⊡ — — — — — — — — — — — — — — — — —		🖬 Makefile	6 KB	File
⊡ 🛅 itl haswell	64	pc.h	17 KB	C/C++ Header
⊡ ⊡ itl nehalem		pciCfgIntStub.c	23 KB	C Source
	64	oricfgStub.c	5 KB	C Source
- 🛅 itl sandybri	dae	Caromcard.s	2 KB	Assembler Source
itl sandybri	dae 64	CromInit.s	25 KB	Assembler Source
⊡ ⊡ itl tolapai	-9-2	h rteError.h	110 KB	C/C++ Header
		h rteOs.h	6 KB	C/C++ Header
		h rteVersion.h	2 KB	C/C++ Header
		🖬 Rtos69diab.a	213 KB	A File
		🖬 Rtos69gnu.a	194 KB	A File
🗈 🦰 VxWin	_	h] rtosLib.h	51 KB	C/C++ Header
	<u> </u>	 ▲		

 Now Start your Workbench, right-click the "Project Explorer" and select "VxWorks Image Projekt".

🚳 Basic Device Development - Wind River Wi	orkbench				<u>_ </u>
File Edit Navigate Search Project Run Ana	alyze Window Help				
] 🖬 📬 • 🔛 🕼 🖻] 🏇 • 🕥 •] 🗸	° •] ■ ¶] ≥ • ₽ • ↔ ↔ • →	*		🖬 💽	
Project Explorer	= 🔄 🔹 📲 🖆 🖛	- 8	🗄 Outline 🛛		
			An outline is not available.		
New	😭 Project				
Market Import	🕅 Wind River Workbench Project		P Terminal		~
Export	😰 VxWorks Boot Loader / BSP Project				
Open Development Shell	😭 VxWorks Downloadable Kernel Module Project	iscri	ption	Resource	Path
S Refresh F5	😭 VxWorks Image Project				
Build Options	F VxWorks ROMFS File System Project				
📲 Remote Systems 🖾 🛛 🔐	Process Project				
E E Local	S VxWorks Shared Library Project				
Wind River Registries	K VxWorks Source Build (Kernel Library) Project				
Let - Local Files	🕑 User-Defined Project				
vxsim0 (Wind River VxWorks 6.9) vxsim1 smp (Wind River VxWorks 6.9)	R Native Application Project				•
	😭 Example]	45M of 98M		
	Cther	Ctrl+N			

• Set the project configuration.

New VxWorks Image Project	📎 New VxWorks Image Project 📃 🔍
Project Create a new VxWorks image project with all available kernel build specs.	Project Setup Base the new project either on an existing project, or on a board support package and a tool chain.
Project name: VxWn Cocation C Create project in workspace C Create project at external location Directory: CI\WindRiver\workspace\VxWin Bcowse Bcowse	Setup the project Based on a board support package Project: Browse BSP: VxWin Address mode: 32-bit kernel Tool chain: Image: Comparison of the supervision of the super
< Back Enish Cancel	< Back Next > Einish Cancel

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• Optionally SMP support can be enabled.

🕥 New VxWorks Image Project 📃 🗖	×
Options Select the options to be used.	
Options Select All Select All Select All Support in kernel Support in kernel Debug enabled and compiler optimizations disabled Debug enabled and normal compiler optimizations enabled	
Cancel Cancel	

- Press "Finish" to create the project.
- After "Build" the project the file "vxWorks.bin" should be created in the "Default" directory below your project directory.



This file should be copied into the VxWin installation after renaming the original file:

😪 🕞 🔻 🕊 VxW 🕨 PENTIUM 👻 🍫	PENTIUM4gnu_SMP durchsuchen
<u>D</u> atei <u>B</u> earbeiten <u>A</u> nsicht E <u>x</u> tras <u>?</u>	
Organisieren 🔻 📄 Öffnen Brennen Nei	uer Ordner 💠 🖬 🔞
VxWin Bin VxWorks54 VxWorks55 VxWorks64 VxWorks66 VxWorks67 VxWorks68 VxWorks68 VxWorks68	Name vxWorks.bin vxWorks_org.bin
PENTIUM4gnu_SMP	4
vxWorks.bin Änderungsdatum: 30.01.2015 BIN-Datei Größe: 2,16 MB	14:51

In this example the VxWorks 6.9 SMP file is replaced so in System Manager the RTOS "VxWorks 6.9 SMP" has to be selected to start the new image.

3.4 How to debug an Application

Please see chapter "5.1 Debugging - Using Tornado/Workbench with VxWin" for details.

3.5 How to debug the BSP

Please see chapter "5.1 Debugging - Using Tornado/Workbench with VxWin" for details.

4 Example Applications

4.1 General

To make your initial experiences in working with VxWin and VxWorks go smoothly, a number of example application programs have been provided with the product release. Some are intended as exercises help familiarize you with various system features; others are useful tools. Much of the code can serve as model software that can save you time in developing your own applications.

The example applications are located on the product release CD-ROM at CD:\SDK\Examples. Per default, setup copies the sample application programs to the directory: C:\Program Files\acontis_technologies\VxWin\SDK\Examples.

4.2 Building a Windows application

The Microsoft Windows example applications are located in "C:\Program Files\acontis_technologies\VxWin\SDK\Examples\Windows". You can create and debug these applications with Microsoft Visual Studio 2005 or newer. A short description how to build the examples can be found in this directory (file HowToBuildTheExamples.txt).

4.3 Building a RTOS (VxWorks) application

The VxWorks example applications are located in "C:\Program Files\acontis_technologies\VxWin\SDK\Examples\VxWorks". A short description how to build the examples can be found in the directory of each example (file HowToBuildTheExamples.txt).

4.4 List of examples

These examples are based on the RTOS-Library. For more information about this library, see the RTOS VM User Manual.

Windows	RTOS (VxWorks)	Description
CSharpDemo	-	The C# demo shows you how to use RtosLib functions from a C# application.
EventDemo	EventDemo	Use the Shared event demo to experiment with Shared Events between two OS.
-	FileDemo	The File demo shows you how a WinCE application can remotely access Windows file system.
InterlockDemo	InterlockDemo	The interlock demo shows you how to use the InterlockedCompareExchange function to synchronize shared memory access works between two OS.
MsgQueueDemo	MsgQueueDemo	Use the Message queue demo to see how a simple communication channel can be established between two OS using a shared memory based message queue to transfer data.
NotificationDemo	NotificationDemo	The Notification demo shows you, how to receive notifications when a blue screen error or other system events occur. The available events depend on the OS.
PipeDemo	PipeDemo	Use the Pipe demo to see how a simple communication channel can be established between two OS using a shared memory based pipe to transfer data.
ShmDemo	ShmDemo	Use the Shared memory demo to see how a simple communication channel can be established between two OS using Shared Memory to transfer data.
SocketDemo	SocketDemo	Use the Socket demo to see how a simple communication channel can be established between two OS using shared memory based socket to transfer data.
-	StdioFileDemo	The File demo shows you how a WinCE application can remotely access Windows file system using the C standard library functions.
UploadDemo	-	Use the Upload demo to see how a simple application can start and stop a RTOS.

5 User's Guide

5.1 Debugging - Using Tornado/Workbench with VxWin

The following debugging methods may be used:

Task-Mode Debugging

Customarily, Tornado/Workbench is used to debug one application task at a time. For this kind of debugging, a debugger must be initially attached to a task.

Thereafter, the user can display and modify variables, perform stepped execution, or let the task run to a breakpoint.

In this debugging-mode, when a breakpoint is reached, only the task being tested is stopped, the rest of the system continues to run.

For detailed discussions of how to use task-mode debugging, refer to the Tornado User's Guide (especially Chapter 10), or the Workbench User's Guide (beginning at Chapter 18).

To debug Interrupt Service Routines (ISR's) or multiple threads, however, system-mode debugging must be used, as described in the next section.

System-Mode Debugging

System-mode debugging (external-mode debugging) is typically used to develop Interrupt Service Routines (ISR), because IRSs run outside of any real-time task context. It is also used whenever multiple threads are to be simultaneously debugged.

Under system-mode debugging, whenever a breakpoint is reached, the entire target system is stopped. For this reason, when using system-mode debugging, the Tornado/Workbench debugger must be hosted on an external system, not on the VxWin system.

For more detailed information about system-mode debugging, please refer to the Tornado User's Guide or the Workbench User's Guide.

5.1.1 Two development arrangements

To develop software for VxWin, the user may choose either of two basic host/target configurations:

• <u>The one-system method</u>: Develop a VxWorks application under Windows and then run it under VxWin on the same computer...

-or-

• <u>The two-system method</u>: Develop a VxWorks application on one computer and run it on another computer, on which VxWin is installed. If you choose the two-system method, you can use a single- or a double-network technique.

In the following discussion, reference is made to a "target server" and a "runtime" or "target" agent. These debugging elements are part of Tornado/Workbench. For details on how to configure and use them, please refer to Wind River's *Tornado User's Guide* or *Workbench User's Guide*.

5.1.2 The one-system method

In a combined host/target system the connection between Windows and the target is made using the VxWin Virtual Network.



In this configuration, the development software runs under Windows and the runtime target's components run under VxWorks, which was installed on the same PC. The development process is as follows:

- Step 1: Build a VxWorks image.
- Step 2: Using the Uploader Utility, transfer the VxWorks image into the program memory.
- Step 3: Under Windows, create a target-server connection to a VxWorks target agent.
- Step 4: Download the application and start debugging it.

5.1.3 The two-system method

When host and target systems are physically separated, the connection between them can be made using either of two techniques.

<u>Two systems – three network adapters</u>

Using this technique (as illustrated below), the target system has two network adapters, one for Windows alone and one for VxWin:

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Development components are installed on the host computer -PC1, while runtime components are installed on the target computer -PC2. The development process is as follows:

- Step1: Build a new VxWorks image on PC1.
- Note: As shipped, VxWorks images may not contain support for a network adapter
 Step 2: To make the image on PC1 accessible from PC2, you must manually create a
- network share on PC1. Then, using the Uploader Utility on PC2, load the VxWorks image.
- Step 3: Create a target-server connection on PC1 to a VxWorks target agent on PC2.
- Step 4: Download the application and start debugging it.

Two systems - two network adapters

Using this technique, the target system has only one network adapter which is used by both Windows and VxWorks. The dual use of one adapter is accomplished by using "packet routing," described elsewhere in this manual. See Packet routing: VxWorks Uses the Windows network adapter. Packet routing ensures that all data packets sent to or originating from VxWorks on PC2 will be sent through PC2's Ethernet adapter.



Here too, development components must be installed on PC1 while run-time components must be installed on PC2.

5.2 Accessing PCI cards

When PCI cards shall be controlled by VxWorks the PC will have to be partitioned first.

By default, all hardware belongs to Windows.

To being able to access hardware from within VxWorks, it will have to be separated from Windows. This is described in detail in the RTOS VM User Manual.

After separation is done, the usual methods within VxWorks can be used to access PCI cards. See the VxWorks manuals for more information.

Within the System Manager, all devices assigned to a RTOS will appear below RTOS \rightarrow Settings \rightarrow Devices:

A System Manager V2.4 Build 3	
<u>F</u> ile <u>V</u> iew <u>T</u> ools <u>R</u> un <u>H</u> elp	
🗢 S 🖌 🖌 🖷 🖸 🖉	
 My Computer Global Settings CPU Assignment Realtime Analyze Realtime Optimizations Devices Mission Display adapters IDE ATA/ATAPI controllers 	RTOS PRO/100 compatible PCI card Vendorld 8086 Deviceld 1229 (Bus 04 Dev 07 Fct 00) Interrupt is enabled Assign to Windows Image: TRQ Property: Legacy
 ✓ IEEE 1394 Bus host controllers ✓ IEEE 1394 Bus host controllers ✓ Storage controllers ✓ Storage controllers ✓ Universal Serial Bus controllers ✓ Files Memory Mapped Files 	IRQ Property: MSI IRQ Property: None Properties
 Memory Areas RTOS #1 (Microsoft Windows CE 6.0) Settings Configuration Devices at RTOS PRO/100 compatible PCI card at RTOS Serial port (COM1) Applications 	Legacy Interrupt Usage Table 1 Standard PS/2 Keyboard 4 RTOS Serial port (COM1) 8 System CMOS/real time clock 10 Realtime OS Virtual Network Adapter (IRQ) #3 Realtime OS Driver (IRQ) ¥3
Show All Devices ORTOS Running	Workspace C:\Program Files\acontis_technologies\CeWin\workspaces\default

5.3 RTOS Library

The RTOS library provides higher-level communication services for synchronizing Windows with VxWorks or to exchange data between the operating systems. The RTOS library is based on VMF-functions, which provide the basic communication functionality.

A description of the RTOS Library can be found in the document RtosVM User Manual chapter 8.1.

5.4 Virtual Network

Using a virtual network connection Windows and VxWorks can set up TCP or UDP communication without adding additional hardware.

5.4.1 Windows 7 / 8 / 10 configuration

A detailed description how to set up the Windows side of the network can be found at the RTOS-VM user manual.

5.4.2 VxWorks configuration

5.4.2.1 Bootline

To configure VxWorks for networking, the user should enter his own prototype boot line in the VxWin configuration file (*general.config*).

When VxWin starts up, the Uploader Utility copies the prototype boot line from the configuration file into memory at a predefined address. Then, when VxWorks boots, it reads and interprets the network parameters in that boot line. Among the various parameters that must be specified are: network addresses, network devices, host-computer designation, script filename, and so on.

The following describes the general VxWorks boot line syntax:

```
bootdev(0,procnum)hostname:filename h=# e=# b=# g=# u=userid pw=passwd
tn=targetname s=startupscript o=other
```

Parameter		<u>Description</u>
bootdev	=	"shm" for the Virtual Network, "ultra" for the SMC Elite Ultra Ethernet adapter card, and so on. (Refer to Wind River documentation.)
procnum	=	The processor number of the Virtual Network. (Must always be 1.)
hostname	=	The name of the host computer from which the boot will be performed.
filename	=	The filename of the VxWorks image to be executed.
е	=	The IP address of the Ethernet port. This field can have an additional partial net-mask. <inet_adrs>:<subnet_mask></subnet_mask></inet_adrs>
b	=	The IP address of the VxWorks Virtual Network interface. This field can also have an additional partial net-mask.
h	=	The host's Internet-Address.
g	=	The Internet address of the host system's gateway. <u>Note</u> : When the host is on the same network, this field should be left blank.
u	=	A valid user-name on the host.

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Parameter		Description
рw	=	The user's password on the host system. When specified, FTP will be used for data transfers.
tn	=	The name of the VxWorks target system.
S	=	The name of a script file containing commands in text form to be interpreted by VxWorks Target Shell Interpreter during startup. This script can access the following "o" parameters.
0	=	"other" – a character string that may be accessed either by commands in the –s script file or a task under VxWorks. This string may be used, e.g., to parameterize the boot process.

<u>Note</u>: Internet addresses are specified in "dot" notation (e.g., 90.0.2) <u>Note</u>: The order of assigned parameters is arbitrary.

<u>Example</u>: The following boot line could be used to configure the Virtual Network:

vnet(0,1)pc:VxWorks h=192.168.0.1 e=192.168.0.2 u=target pw=vxworks

<u>Note</u>: IP addresses must, of course, match those used on the Windows side of the Virtual Network.

5.4.2.2 Virtual Network adapter address

The network adapter address [also known as the MAC (Media Access Control) address] is generally set in accord with the following syntax:

AA-BB-CC-DD-EE-00

You can use a parameter in the VxWin configuration (*general.config*) file to specify a network address for your Virtual Network card. Refer to NetworkAddress in Global parameters in the VxWin configuration file. For information on setting MAC addresses on both VxWorks and Windows, also refer to Setting MAC Addresses for the Virtual Network Driver.

5.4.2.3 Virtual Network adapter polling or interrupt mode

The virtual network adapter can be configured either for polling or interrupt mode (*general.config*).

[Rtos]

"VnetPollPeriod"=dword:0 ; polling period in ticks (0 = interrupt mode)

5.5 Interrupts and Exceptions

Hardware devices, with the help of their associated controller/adapters, generate interrupts to signal when they require attention from the CPU.

Upon receiving an interrupt signal, the CPU will promptly set aside its current processing to attend instead to the needs of the interrupting device. This rapid response assures that real-time operating systems respond to external events as quickly as possible.

<u>Note</u>: While peripheral devices may share an interrupt under *either* of the two operating systems, any attempt to share an interrupt *between* the two systems under VxWin will result in unpredictable and certainly incorrect system behavior.



All interrupts for VxWorks as well as those for Windows are connected to the PC's Interrupt Controller which, upon the occurrence of a connected event, will interrupt the CPU.

VxWin guarantees that interrupts controlled by VxWorks always have a higher priority than any program or any interrupt under Windows. If VxWorks is running on the same CPU core as Windows (shared mode) then within microseconds after an interrupt occurs, whichever Windows program or lower priority VxWorks task was running is stopped and control is transferred to the VxWorks Interrupt Service Routine (ISR) that the user assigned to that interrupt during initialization.

After the ISR finishes executing, control is returned to the VxWorks kernel, which then checks to see if any other real-time threads are ready to run. If that is the case, the kernel will activate the now-ready-to-run task. VxWorks tasks will continue to run until all are suspended or blocked. After which, the system will enter the VxWorks idle loop, which in turn returns control to Windows (when running in shared mode).

Since Windows is re-activated only when all VxWorks tasks are idle, one could say that Windows runs as if it were part of VxWorks idle loop (when running in shared mode).



State diagram of VxWin (shared mode)

5.5.1 Exception-Handling (shared mode)

The processor hardware is able to detect and respond to certain kinds of serious errors such as faulty addressing (page/segment errors) or floating-point processor errors (such as divide by zero). This class of hardware errors are generically called exceptions. When an exception occurs, it triggers a high-priority interrupt. The correct reaction to the occurrence to such an exception depends on which operating system, Windows or VxWorks, was running at the time. To enable them to respond, each in its own way, VxWin provides two different exception tables (IDT), one for each system context, which it swaps at appropriate times. Whenever a real-time interrupt activates VxWorks, VxWin replaces the Windows exception table with one defined for VxWorks. Correspondingly, whenever control is returned to Windows, the Windows exception table is likewise restored.

Exception-table-swapping, is only one of the mechanisms VxWin implements to enable the operating systems to share a single execution platform.

5.5.2 usrRoot Task Exceptions (shared mode)

If the usrRoot task (VxWorks) generates an exception, i.e., a page error, the entire system will crash. While experience has shown that this occurs only

infrequently, when it does occur, the most common reason for it, it has been determined, is that VxWorks had been incorrectly configured.

5.5.3 Connecting an ISR to an interrupt

After VxWorks has started, you can use the VxWorks *intConnect()* function to connect an Interrupt Service Routine (ISR) to a specific interrupt level. In the following example, the Interrupt Service Routine *proc* is connected to interrupt level 7. Thereafter, interrupt level 7 in the interrupt controller is enabled.

```
intLvl = 7;
intNum = INT_NUM_GET(intLvl);
intVec = INUM_TO_IVEC(intNum);
intConnect(intVec, proc, param);
sysIntEnablePIC(intLvl);
```

<u>Note</u>: *intLvl* is the interrupt level, *intNum* is the interrupt number, *intVec* is the interrupt vector. The macro *INT_NUM_GET* supplies the required interrupt vector from either of two arrays in accord with the interrupt controller type. One can also use predefined macros, such as *INT_NUM_COM1* or *INT_VEC_COM1*, which are defined in the header file *configInum.h*. While *proc* is the C routine to be called in response to the interrupt (the ISR), *param* is a parameter to be passed to the routine each time it is called.

For more information about this topic, see Wind River's documentation, *VxWorks Programmer's Guide*.

5.5.4 Additional interrupt functions – VxWorks

When your VxWorks application must use other functions that pertain to interrupts, you should use the standard functions as defined by Wind River for VxWorks. Please refer first to the *intLib* and *intArchLib* libraries in the Wind River documentation (*VxWorks OS Libraries API Reference* and *VxWorks Programmer's Guide*).

5.6 VxWorks System overload

It is possible to overwhelm a real-time system with so many interrupts that it can not dispose of the resulting workload. This causes work-queues to become full so that they cannot accept additional entries. For most real-time applications this would lead to an overall system malfunction.

<u>Note</u>: This is not a limitation of VxWin. System overload is a condition that can arise in any real-time system.

Therefore, it is important that real-time system designers plan their systems carefully, taking into account the worst-case demands that will be placed on it, so that this kind of error does not occur.

When a native VxWorks detects an internal work-queue overrun, it typically reboots itself. After which, the BSP routine *sysToMonitor* is called. If we were dealing with a VME system, for example, under these circumstances, the ROM-monitor would be called.

But a VxWin system has no ROM-monitor. In this case, a call to *sysToMonitor* causes all interrupts to be deactivated and a message box will be shown by Windows.

5.7 VxWin – Board Support Package

5.7.1 Introduction

The VxWin board support package (BSP) provides special adaptations for Wind River's VxWorks to the run-time environment of VxWin.

Several files and functions are designated by Wind River Systems as platform or run-time specific such as the *sysLib.c* file.

This manual describes such components and additional extensions to support the operation of VxWorks concurrently with Windows.

Important:

The BSP is not part of the VxWin setup program. Please contact Customer Support for the latest BSP adequate to your desired VxWorks Version.

5.7.2 Important files in the VxWin BSP

In the following sections, some important files included in VxWin Board Support Package have been listed along with brief explanations of their purpose. The files are grouped primarily to reflect their general role in the system and secondarily to reflect their file-type. Only files that were developed or modified for VxWin are listed here.

VxWin Source Files

File	Description
RtosLib.h	All definitions pertinent to VxWin Interface, including those for shared events and shared-memory API.
vxwinVioSio.c	The SIO driver for the VMF VIO interface
vxwinVnetEnd.c	The END driver for the VMF VNET interface
vxwinConsoleLoader.c	Determine the console device using the following entries of the VxWin config file: [Rtos] "Console"="/vio/0" ; use "/tyCo/0" for first COM port.
vxwinNetworkLoader.c	Attach END interface to the network stack using the following entries of the VxWin config file: [Rtos] "AddNetwork0"="fei0:192.168.100.1:255.255.255.0" "AddNetwork1"="gei0:192.168.200.1:255.255.255.0"
vxwinNetworkLogger.c	Handle the network logging functionality using the "LogNetworkFilter" entries in the [Rtos] section of the VxWin config file.
vxwinRamDiskMounter.c	Automatically mount a RAM disk device to an eventually VMF general purpose shared memory area named "RamDisk". Please refer to RTOS VM User Manual. [SharedMemory\RamDisk] "Name"="RamDisk" "Description"="RAM disk example configuration" "Base"=dword:200000 "Alignment"=dword:0 "Size"=dword:300000 "File"="C:\\RamDisk.img" "Initialize"=dword:2 "Save"=dword:1 "AccessDefault"=dword:0 [SharedMemory\RamDisk\AccessModes] "0"=dword:1

Additional VxWorks Test Tools

File	Description	
vmfInterface.h	The VMF interface API header file	
vxwinDemo.c	A demo-program (e.g. latency measurement) Start the demo: function demoStart() Stop the demo: function demoStop()	
vxwinCpuMon.c	CPU Monitoring. Displays performance data for each CPU. Start monitoring: function vxwinCpuMonStart() Reset monitoring: function vxwinCpuMonReset() Stop monitoring: function vxwinCpuMonStop() Show data: function vxwinCpuMonShow() To get performance data on exclusive cores, enable 'IdleOs' by setting an appropriate value for "ProcessorMask" in the section [Idle] in the config- file.	

Additional VxWin Libraries

File	Description	
RtosXxYy.a	VxWin kernel library for vxworks version Xx and tool Yy (diag or gnu)	
RtosXxYy.so	VxWin shared object library for vxworks version Xx and tool Yy (diag or gnu)	

5.8 VxWin – Board Support Package for VxWorks 7

5.8.1 Development Host: Copy VxWin platform and BSP

Copy the VxWin platform directory vxwin_common into %WIND_BASE%\vxworks-7\pkgs_v2\os\psl\intel

Copy the VxWin BSP directory vxwin_generic into %WIND_BASE%\vxworks-7\pkgs_v2\os\board\intel

5.8.2 Target System Configuration

The following VxWin configuration settings are mandatory:

- Assure the VMF is located below physical address 0x20000000
- Minimum memory size is 48 Mbyte

See settings located in folder VxWin-TargetConfig, file vxworks7.config:

```
[Vmf]
"AddressMax"=hex:FF,FF,FF,1F,00,00,00,00
[Rtos]
"MemorySize"=dword:3000000
```

5.8.3 Shipped Binary image

You can find a bootable VxWorks 7 binary image (vxWorks.bin) in folder VxWin-Target

5.8.4 VxWorks Source Build VSB

Create a new VxWorks Source Build Project. It has to be based on the vxwin_generic BSP, the PENTIUM4 CPU has to be selected. SMP is not supported and must be unselected (UP support in Libraries).

W New VxWorks	s Source Build Project	—	
Project Setup Base the new pro CPU type.	oject either on an existing	g configuration, a board support package, or a	
-Setup the proje	ct		
Based on a Vx	Works 7 board support pa	ackage	~
BSP:	vxwin_generic_	✓ Show latest v	ersions only
Active CPU:	PENTIUM4		~
Baseline:	SR0630 2019-11-08		
buschner			
Address mode:	ILP32 32-bit libraries		\sim
Options:	Toolchain:	Ilvm 8.0.0.2	~
	Floating point setting:	hard	\sim
	Endian setting:	little	\sim
	Processor mode:	UP support in Libraries	~
	Debug mode:	Off, and normal compiler optimizations enabled	I ~
	IP version setting:	IPv4 only enabled libraries	~
	VSB profile:	Minimal layers included	~
Link in source	es to project		
Setup informati	ion		
Base location:	vxwin_generic_		
?	< <u>B</u> a	nck <u>N</u> ext > <u>F</u> inish	Cancel

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5.8.5 VxWorks Kernel Image Project VIP

Create a new VxWorks Kernel Image Project. It has to be based on the VSB, which has been created before.

W New VxW	W New VxWorks Image Project					
Project Setu Base the nev board suppo	P v project either on a ort package and too	n existing project, l chain selections.	or on a source bu	ild project providin	g	7
Setup the p	roject					
Based on a	a source build proje	ct			~	
Project:	VxWin_vsb_gpp_llv	/m			∨ Browse	
BSP:	vxwin_generic_				~	
Tool chain:	llvm				~	
Options:	Address mode:	ILP32 32-bit libra	ries		~	
	Processor mode:	UP support in Lib	oraries		~	
	Debug mode: Off, and normal compiler optimizations enabled \sim					
Setup information Base location: C:/VX70SR630GPP/workspace/VxWin_vsb_gpp_llvm						
?		< <u>B</u> ack	<u>N</u> ext >	<u>F</u> inish	Cancel]

Supported Components:

- The VxWin Rtos library
 FOLDER_VXWIN_RTOSLIB
- Two virtual timers (for sysClk and auxClk): INCLUDE_VXWIN_VXB_TIMER0/1
- One virtual serial channel for the Kernel Shell: INCLUDE_VXWIN_VXB_VIO_SIO
- Virtual network driver: INCLUDE_VXWIN_VXB_VNET_END
- BSP Debugging (for extended BSP debugging): INCLUDE_VXWIN_BSP_DEBUG

All other VxWin components MUST NOT be included/enabled. The resulting VxWorks image will be located in %VIPDIR%\default\vxWorks.bin

Ø VxWin_vip_gpp_llvm_dev ☆				
Components				⇒ ?
Description	Name	Туре	Value	^
✓	FOLDER_VXWIN			
✓ I END Ethernet drivers	FOLDER_VXWIN_END			
3COM END driver	INCLUDE_EL_3C90X_END			
FEI END driver	INCLUDE_FEI_END			
✓ I VxWin BSP Debugging	FOLDER_VXWIN_BSP_DEBUG			
BSP Debugging Components	INCLUDE_VXWIN_BSP_DEBUG			
🗸 🎒 VxWin RtosLib	FOLDER_VXWIN_RTOSLIB			
Communication Subsystems	INCLUDE_VXWIN_RTOSLIB_COMM			
Real-time process support	INCLUDE_VXWIN_RTOSLIB_FOR_RTP			
Time Synchronization	INCLUDE_VXWIN_RTOSLIB_TIMESYNC			
✓ I VxWin drivers	FOLDER_VXWIN_DRIVERS			
✓ Ø VxWin virtual IO driver	INCLUDE_VXWIN_VIO_SIO			
Wumber of virtual IO channels	N_VXWIN_VIO_SIO_CHANNELS	uint	1	
VxWin virtual network END driver	INCLUDE_VXWIN_VNET_END			
✓ ₩ VxWin drivers for VxBus	FOLDER_VXWIN_VXB_DRIVERS			
✓ ₽ VxWin timer drivers	FOLDER_VXWIN_VXB_TIMER			
VxWin timer stamp driver	INCLUDE_VXWIN_VXB_TIMESTAMP			
🗸 💕 VxWin timer0	INCLUDE_VXWIN_VXB_TIMER0			
🐷 timer 0 maximum freq	VXWIN_VXB_TIMER0_MAX_FREQ	uint	5000	
😨 timer 0 minimum freq	VXWIN_VXB_TIMER0_MIN_FREQ	uint	19	
🗸 💕 VxWin timer1	INCLUDE_VXWIN_VXB_TIMER1			
😴 timer 1 maximum freq	VXWIN_VXB_TIMER1_MAX_FREQ	uint	5000	
🐷 timer 1 minimum freq	VXWIN_VXB_TIMER1_MIN_FREQ	uint	19	
🗸 💕 VxWin virtual IO VxB driver	INCLUDE_VXWIN_VXB_VIO_SIO			
Number of virtual IO channels	N_VXWIN_VIO_SIO_CHANNELS	uint	1	
💕 VxWin virtual network VxB END driver	INCLUDE_VXWIN_VXB_VNET_END			
V State Network interface loader	INCLUDE_VXWIN_NETWORK_LOADER			
😨 IP Driver Control Limit	IP_MAX_UNITS	uint	2	
				~

5.8.6 Target connection to debug a DKM

At the very first time a "New Connection" has to be created. Properties see below screenshot.

system Development - vxwin - Wind River Workbench		
Search Project Run Window Help		
🗟 🔽 vxwin 🔍 🕈 🖓 🖡 🕶 🔊 🗸	▓▏▣〕☆▼ ○▼ ∿▼ ₩ ♥ ፇ ↗↓♪ № 圓 面 ₺ ▼ ₽ ↓ ↓ ↓ ↓ ■	
V vxwin 🛃 🕷 🗸 🗢 E	c dkm.c 🖻 LinkOsPlatform.h 🖻 EcDevice18254x.cpp 🕼 18254x.cpp 🖒 LinkOsLayer.h	🔂 LinkOsLayer.cpp 🛛 🕅 vxwin 🕅 💿 vxwinVIP
/ori Open Connection Details s (v 💢 Delete	VxWorks Connection	
EN Rename	Target Target Twee	Connection Mode:
54x.cpp 54x.h	Running Target Core Dump VW/ork: Simulator	Application Mode Stop Mode
e.h	Description:	
s.h	You are connecting to an already booted VxWorks system. This connection allows to use the task-mode debu	gger, application download and launch, as well as System V
na.h		
.cpp		
.n srm.h		
lef	Configuration	
7 Downloadable Kernel Module Project)	Target Address: 9 192.168.0.210:1534	
s (vxwinVSB_PENTIUM4gnu - debug)	Kanad Cumbert Eller	
rtialimage (hello_partialimage.o)	Kernel Symbol File:	
no.outy		
	Start debugger after connect	
ENITH IN AA		

5.8.7 Debugging a DKM

The VIP has to built with the appropriate Debug Agents included:

Components	
Description	Name
> 🗱 C++	FOLDER_CPLUS
✓	FOLDER_TOOLS
Application Mode Agent components	FOLDER_DEBUG_AGENT
> 💣 Application Mode Agent (default)	INCLUDE_DEBUG_AGENT
Application Mode Agent Banner	INCLUDE_DEBUG_AGENT_BANNER
Application Mode Agent Start	INCLUDE_DEBUG_AGENT_START
Boot application A	FOLDER BOOT APP

After booting the VIP, select the previously created connection (in the example below its name is "vxwin") and press the Connect button.

/stem Development - Kernel configuration editor of project vxw

se <u>a</u> rch	Project	<u>R</u> un	Window	He	Ip			
I V.	vxwin			۷	Ν	59	6	· 🔊 🤊
				•	£	Conne	ct 'vxw	/in'

Select the DKM project. Press the "Target Actions" button and select "Run/Debug Kernel Task...".

River Workbench



orks 7 Downloadable Kernel Module Project)

5.9 VxWorks 7 Kernel Image with 512 Mb memory

To build a VxWorks 7 image with 512 Mb, following settings have to be adjusted:

• File systemmgr_user.config, located in folder VxWin-TargetConfig:

[Rtos]

"MemorySize"=dword:20000000; 512Mb

This entry is only necessary if a system without VT-x is used:

```
"MemoryStartAddress"=dword:10000000; 256Mb
```

If a system without VT-x is used, these Components settings have to be changed in the VIP project:

• Components in the VIP project:

🗆	Mem	ory (default)	FOLDER_MEMORY		
-	🔁 M	IMU	FOLDER_MMU		
	E	MMU optimization	FOLDER_MMU_OPTIMIZE		
	Ξ 🧯	32-bit physical address space (default)	INCLUDE_MMU_P6_32BIT		
		Virtual memory page size	VM_PAGE_SIZE	uint	0x1000
	6	🗜 Basic MMU (default)	INCLUDE_MMU_BASIC		
	± (MMU global map (default)	INCLUDE_MMU_GLOBAL_MAP		
	6	Optimize page tables	INCLUDE_PAGE_SIZE_OPTIMIZATION		
-	💕 В	SP memory configuration	INCLUDE_MEMORY_CONFIG		
	- Bas	Boot line address	BOOT_LINE_ADRS		(LOCAL_MEM_LOCAL_
	600	Boot line offset	BOOT_LINE_OFFSET		0x1200
	60	Boot line size (bytes)	BOOT_LINE_SIZE		255
	- Bas	Bootrom copy region	RAM_HIGH_ADRS		0xf0008000
	600	Bootrom initial stack pointer	ROM_STACK		0x7000
	- Bas	Bootrom stub code copy destination address	WARMBOOT_COPY_ADR5		0x500
	600	E820 data offset	BIO5_E820_DATA_OFFSET		0x4000
	600	Exception message address	EXC_MSG_ADRS		(LOCAL_MEM_LOCAL_/
	60	Exception message offset	EXC_MSG_OFFSET		0x1300
	6	First stage text address	FIRST_TEXT_ADRS		0x00008000
	60	GDT offset	GDT_BASE_OFFSET		0x1000
	- Bas	Local memory size	LOCAL_MEM_SIZE		(SYSTEM_RAM_SIZE -
	600	MPAPIC data offset	MPAPIC_DATA_OFFSET		0x2000
	60	Multistage data offset	BOOT_MULTI_STAGE_DATA_OFFSET		0x5000
	90	ROM base address	ROM_BASE_ADRS		0x00408000
	6	ROM size	ROM_SIZE		0x00200000
	5	ROM text address	ROM_TEXT_ADR5		(ROM_BASE_ADRS)
	60	ROM warm boot address	ROM_WARM_ADRS		("unused")
	60	Reserved	RESERVED		0
	900	Runtime kernel load address	KERNEL_LOAD_ADRS		0x00100000
	8	Runtime kernel load address	RAM_LOW_ADRS		0×F0008000
	6	Stack size	STACK_SAVE		0x40
	500	Vector base address	VEC_BASE_ADRS		((char *)LOCAL_MEM_
	900	Virtual memory base address	LOCAL_MEM_LOCAL_ADRS		0xF0000000
	ju ju	Warm reboot bootrom address offset	ROM_WARM_HIGH		0x10

Set LOCAL_MEM_LOCAL_ADRS to 0xF0000000

Set RAM_LOW_ADRS to 0xF0008000

5.10 Multiple Instances

For systems with multi OS configurations, each instance of a VxWorks image must be created separately with individual memory settings. The memory settings herein must correspond to the memory settings in the config file. The maximum needed memory size will be calculated by following formula: size(Image1) + size(Image2) + size(Image3) + ...

Each image could have individual sizes, but the minimum start addresses, etc. depends on the previous image start address and size. It is possible to leave a gap in memory between each image.

Without gap:

```
Off0: [.config:Upload] RteMemoryStartAddress
Off1: Off0 + s0;
Off2: Off1 + s1;
Off3: ...
```

Min. RteMemorySize: s0 + s1 + s2 + ...

With gap (between each image):

```
Off0: [.config:Upload] RteMemoryStartAddress
Off1: Off0 + s0 + g;
Off2: Off1 + s1 + g;
Off3: ...
g: gap of variable size
```

Min. RteMemorySize: $s0 + g + s1 + g + s2 + \ldots$

Remark A: The gap between each image can be of different size. **Remark B:** There could be an initial gap before first image and a gap after last image. In those cases, the RteMemorySize calculation must be adjusted accordingly.

5.10.1 Individual Image Creation

The followings steps are required to get the desired images:

- 1. Create a Wind River VxWorks 6.x Image project based on VxWin BSP
- 2. Go to Kernel Configuration
- 3. Search node memory (default) \ BSP Memory Configuration
- 4. Adjust value: *Physical memory base address* with **Off0** for 1st image, **Off1** for 2nd image, and so on.
- 5. Adjust value: *Runtime kernel load address* with **Off0 + 0x8000** for 1st image, **Off1 + 0x8000** for 2nd image, and so on.
- 6. Adjust value: *local memory size* with **s0** for 1st image, **s1** for 2nd image, and so on.
- 7. Compile image

Hint: The steps 4 & 5 must be adjusted for **each** desired image. Step 6 only, if the size of the images vary.

5.10.2 Further Config File Dependencies

Each configured rtos have a separate entry in the .config file. The offset values (OffX) and the size values (sX) correspond directly to the rtos specific entries in the .config file. **Hint:** Using of the System Manager the values will be automatically adjusted without manual interference!

1st rtos:

```
[Rtos]
    "MemoryStartAddress"=dword:XXXXXXX : Value of Off0!
    "MemorySize"=dword:XXXXXXX : Value of s0!
```

2nd rtos:

```
[Rtos1]
    "MemoryStartAddress"=dword:XXXXXXXX : Value of Off1!
    "MemorySize"=dword:XXXXXXX : Value of s1!
```

3rd rtos:

```
[Rtos2]
    "MemoryStartAddress"=dword:XXXXXXX : Value of Off2!
    "MemorySize"=dword:XXXXXXX : Value of s2!
```

6 Built-in tools and utilities

6.1 RtosService

RtosService.exe is needed when using the RtosLib.lib.

If you look at the default General.config file, you will see that RtosService.exe is automatically launched.

RtosService.exe starts the communication between Windows and WinCE. Also it starts the time synchronisation between Windows and WinCE.

6.2 Boot Line for VxWorks

When VxWorks is booted up in an embedded system, among the first things it does is read a line of text from a pre-defined location in RAM memory¹. This so-called *boot line*² contains parameters that VxWorks uses to dynamically characterize itself during its boot-up phase. Boot-line parameters convey information such as: network addresses, boot device designation, password, and start-up script identifier.

When VxWin is started, it retrieves boot-line parameters from the boot line in the VxWin configuration file. By this means, VxWin users can influence, upon each startup, how VxWorks boots, without having to generate a new image.

¹ The boot line is stored in the variable *sysBootLine* as defined in the VxWorks *sysLib.c* library. The boot line is a simple ASCII text-string.

² "Boot line," a VxWorks term, is described in detail in Wind River Systems documentation.

7 Version History

A general version history containing information about new features, migration hints and improvements can be found in the release notes file "ReleaseHistory.txt".